

**Figure 1**  
(PRIOR ART)

HIGH LEVEL DESCRIPTION OF THE CIRCUIT DESIGN 12

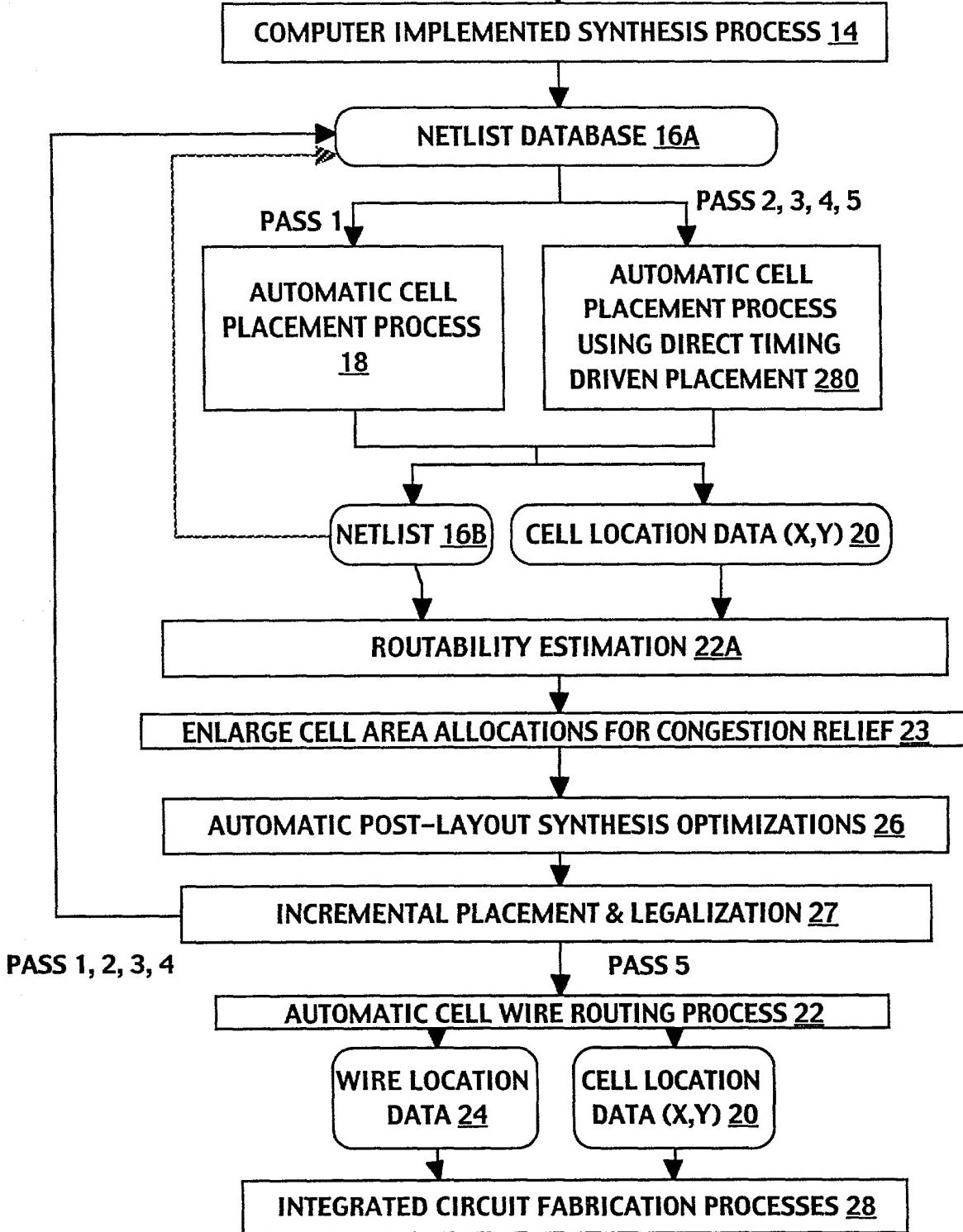


Figure 2

600

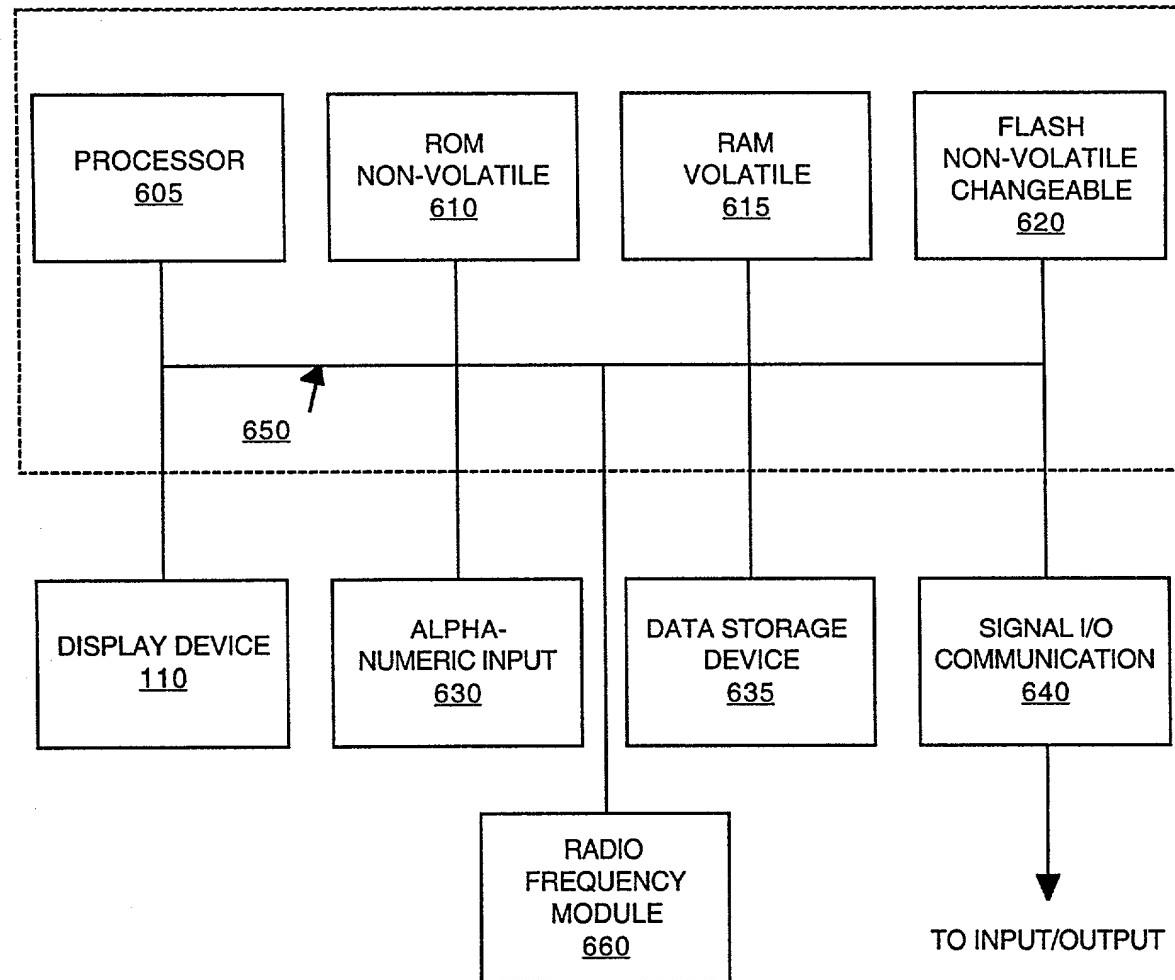


Figure 3